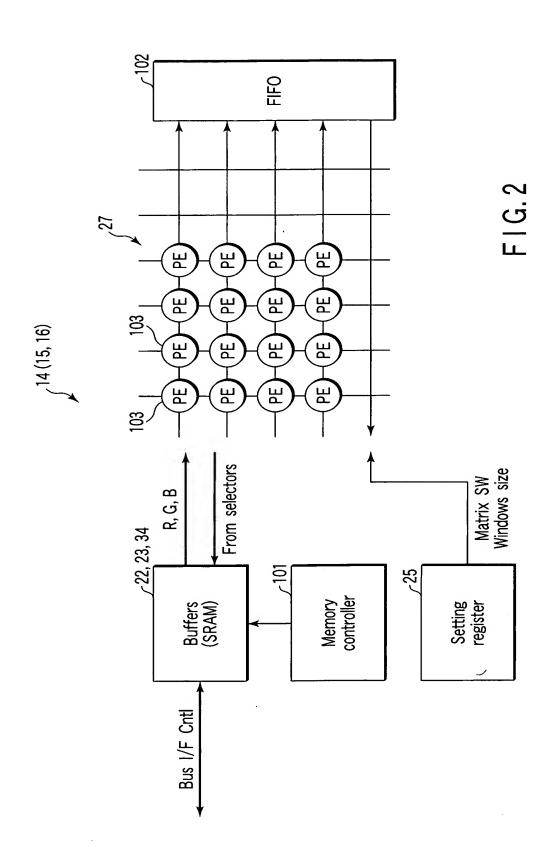
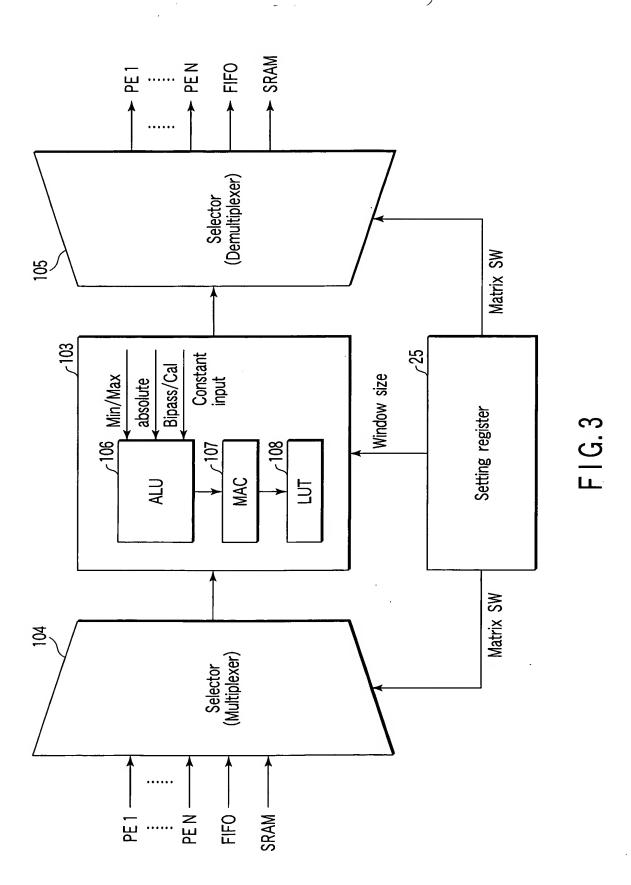
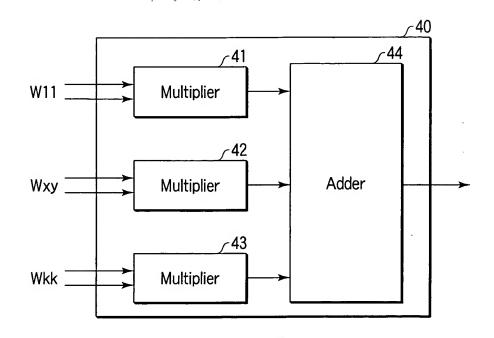


FIG.1

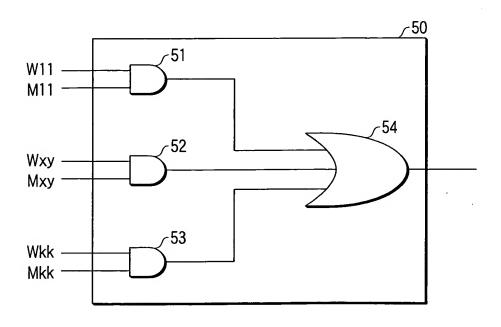
Title: PROCESSOR SYSTEM AND DATA PROCESSING METHOD Inventor(s): Akihisa NAKAMURA Appl. No.: 10/800,631







F1G.4



F I G. 5

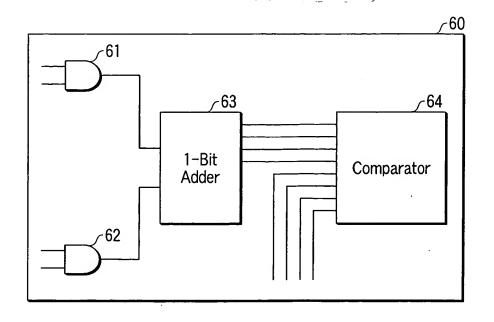


FIG.6

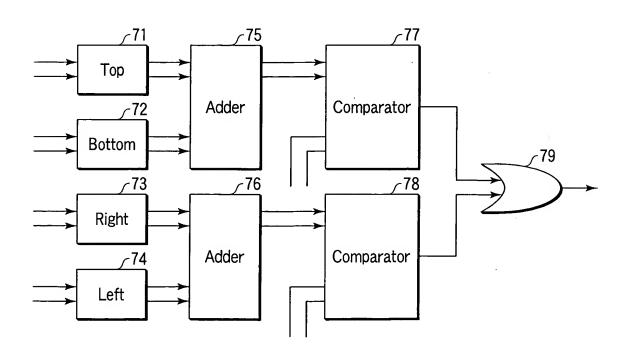
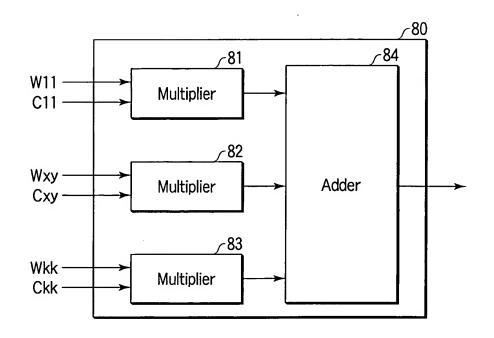
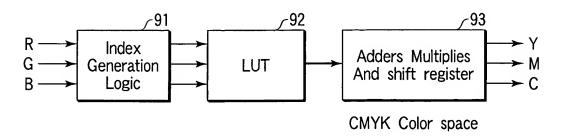


FIG.7



F I G. 8



F I G. 9

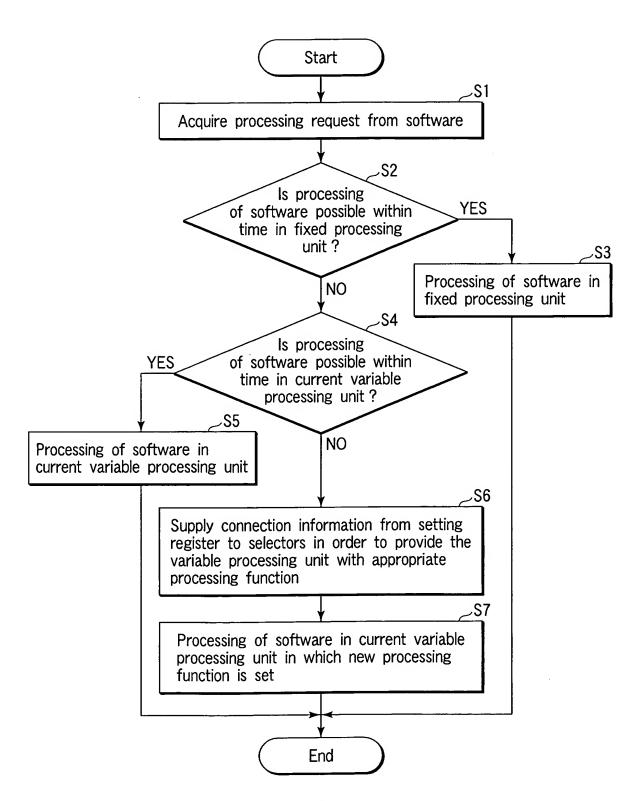


FIG. 10

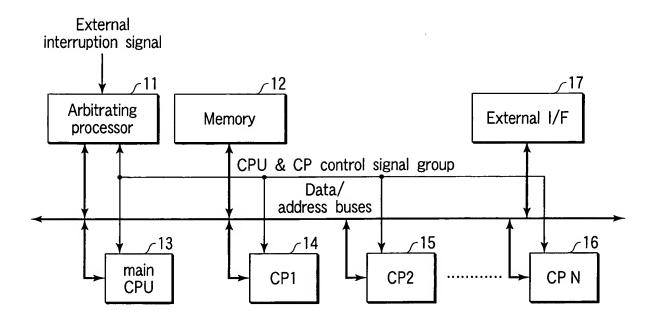


FIG. 11

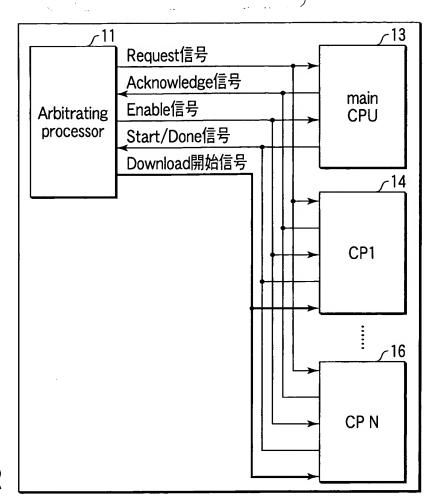


FIG. 12

